

MAY 27 2004
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OFFICIAL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of James J. Brogle et al.

Group Art Unit: 2823

Examiner: George G. Fourson, III

Serial No.: 10/005,513

Filed: November 8, 2001

For: MONOLITHICALLY INTEGRATED PIN DIODE AND SCHOTTKY DIODE
CIRCUIT AND METHOD OF FABRICATING SAME

STATUS CHECK

Honorable Commissioner of Patents and Trademarks,
Alexandria, VA 22313-1450

Sir:

Kindly immediately advise the undersigned of the status of the above-identified application.

Respectfully submitted,

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